

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
APPLICATION FOR UNITED STATES PATENT

In re Patent Application of)	Group Art Unit: 2811
Abdalla Aly Naem et al.)	Examiner: Unknown
Appln. No. 10/315,394)	
Filed: December 10, 2002)	POWER OF ATTORNEY BY ASSIGNEE AND
)	ESTABLISHMENT OF RIGHT OF ASSIGNEE
For: MOS TRANSISTOR AND METHOD OF)	<u>TO PROSECUTE UNDER 37 C.F.R. 63.73(b)</u>
FORMING A MOS TRANSISTOR WITH)	
A LAYER OF SILICON GERMANIUM)	
CARBON)	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

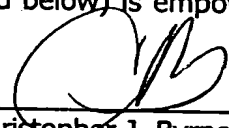
Sir:

NATIONAL SEMICONDUCTOR CORPORATION, assignee of the entire right, title and interest in the above-identified application by assignment dated December 10, 2002, **which assignment is recorded in the U.S. Patent and Trademark Office at Reel 013562, Frame 0662**, hereby appoints the Law Offices of Mark C. Pickering, a firm composed of: Mark C. Pickering (36,239); and the following members of the National Semiconductor Corporation Intellectual Property Department: Christopher J. Byrne (32,204); Eugene C. Conser (39,149); John Maxin (34,668); Coleman F. Reif (38,593); Allen R. Tremain (40,207); Andrew S. Viger (28,552); and Peter Y. Wang (40,452) as our attorney(s) or agent(s) to prosecute the above-identified application, and to transact all business in the U.S. Patent and Trademark Office connected therewith.

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The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the assignee.

Dated: 4/29/03

By: 
Name: Christopher J. Byrne
Title: Director of Intellectual Property and Technology
Licensing

NATIONAL SEMICONDUCTOR CORPORATION

Express Mail Number EV342470706US**COPY**Atty Docket No 100-19800 **PATENT**
[P05402]**DECLARATION FOR PATENT APPLICATION**

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

**MOS TRANSISTOR AND METHOD OF FORMING A MOS TRANSISTOR WITH A LAYER OF SILICON
GERMANIUM CARBON**

the specification of which (check one) X is attached hereto or ___ was filed on ___ as Application No. ___ and was amended on ___ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed	
Yes	No

Number	Country	Day/Month/Year Filed	Yes	No
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I hereby claim the benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) below.

Application Number	Filing Date
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Application Number	Filing Date
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I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Application Number	Filing Date	Status: Patented, Pending, Abandoned
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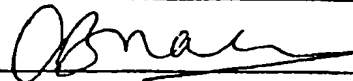
Application Number	Filing Date	Status: Patented, Pending, Abandoned
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Alty Docket No 100-19800 PATENT
[P05402]

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor ABDALLA ALY NAEM

Inventor's signature



Dec. 10 - 2002

Date

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12/10/02

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